	PTO-1449	· -		ATTY. DOCKET NO.:			SERIAL NUMBER:			
(REV. 7-	PATENT AND TRAD	57941.000062 10/716,595 3 APPLICANT(S): MICHAEL FARMWALD et al.								
LIST OF MATERIALS CITED BY APPLICANT										
	Use several sheets if necessar	FILING DATE:		GROUP ART UNIT:						
	Page 1 of 1	November 20, 2003			2818 2827					
			U.S. PATENT D	OCUMENT	r s	CLASS			9	
*EXAMINER INITIAL	3	DOCUMENT NUMBER	DATE		NAME		SUBCLASS	FILIN	G DATE	
TNS	1	5742798	04-21-1998	Goldr	Goldrian					
TNI	2.	4480307	10-30-1984		Budde et al.			<u> </u>		
			OREIGN PATEN							
		DOCUMENT NUMBER	DATE	COUNTRY	RY CLASS AND SUBCLASS		TRANSLATION PROVIDED			
							Yes No		No	
TWS	3.	2000035831 A	02-02-2000	JP			X (Abstract)			
TNI	4.	0 348 113 A2	12-27-1989	EP					X	
		OTHER MAT	FERIALS (Including	g Author, Titl	e, Date,	Pertinent I	Pages, Etc.)			
5. Opposition against European patent 0 525 068, (Application No. 91 908 374.1-2201) (Gerry Translation), 04-15-2004				1) (German & En	glish					
	6.	Communication of the Tech Boards of Appeals in the Op 03								
	7.	Brief Communication in the Opposition against European patent 1 022 642 (Application No. 00108822.8), 04-16-2004								
	8.	Minutes of the oral proceedings of February 10, 2004 in the Opposition against European patent 0 525 068, 02-18-2004								
	9.	9. LU et al., The Future of DRAMs, ISSCC 88, 02-18-2004, pp. 1-2								
	10.	 Opposition against European patent 1022642, (Application No. 0010822.8) (German & English Translation), 03-02- 2004 								
	11. Opposition against European patent 1004956, (Application No. 00010832.4) (German & English Translat 03-2004						anslatio	n), 03-		
	12. Opposition against European patent 1022642, (Application No. 0010822.8) (German & English Translation), 07-2004), 07-15-	
	13. Opposition against European patent 1004956, (Application No. 00010832.4) (German & English Translation), 07-15-2004								n), 07-	
	14.									
	15.	Intel, Electrical Specifications for iAPX 43204 Bus-Interface Unit (BIU) and iAPX 43205 Memory Control Unit (MCU), March 1983								
	16.	16. Intel, Memory Components Handbook, 1985								
	17. Summons To Attend Oral Proceedings Pursuant to Rule 71(1) EPC in the Opposition against Europ 956, 07-20-04						ean pate	ent 1 004		
	18.									
	19.	Decision of February 12, 2004 by the Board of Appeals of the EPO in the Opposition against European patent 0 525 068 (Application No. 91908374.1)								
	20.	 MORGAN, The CVAX CMCTL — A CMOS Memory Controller Chip, Digital Technical Journal, No. 7, August 1988, pp. 139-143 								
	21. FELLIN et al., Intel, Application Note, AP-132, Designing Memory Systems with the 8K x 8 iRAM, June 1							1982		
	22.	PRINCE et al., Semiconduc								
747	23.	Decision of the Technical B	oard of Appeal 3.5.1	of February 1	2, 2004	in the Oppo	sition against Eur	ropean p	patent 0	

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

EXAMINER

DATE CONSIDERED

4/14/98